

FIG. 1

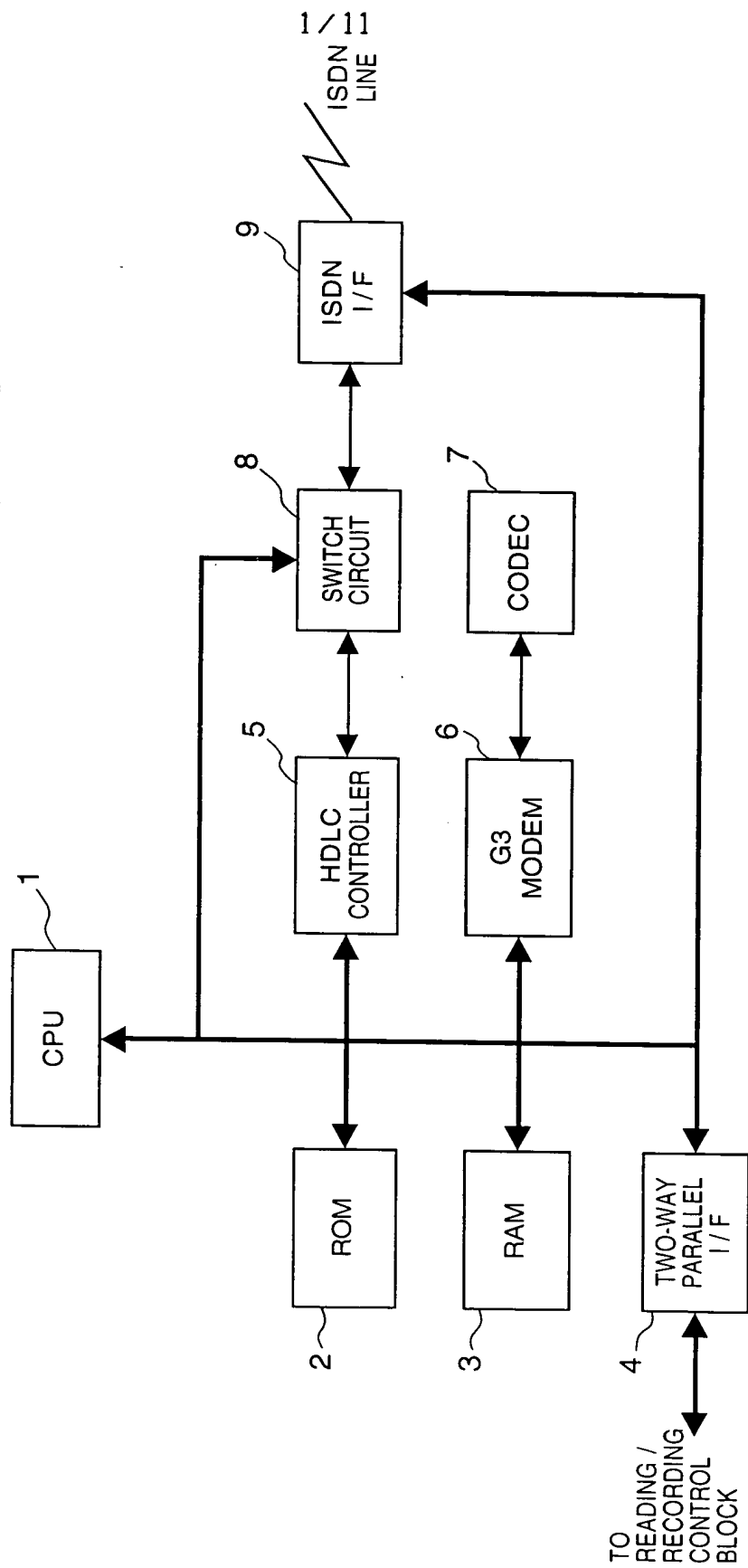


FIG. 2

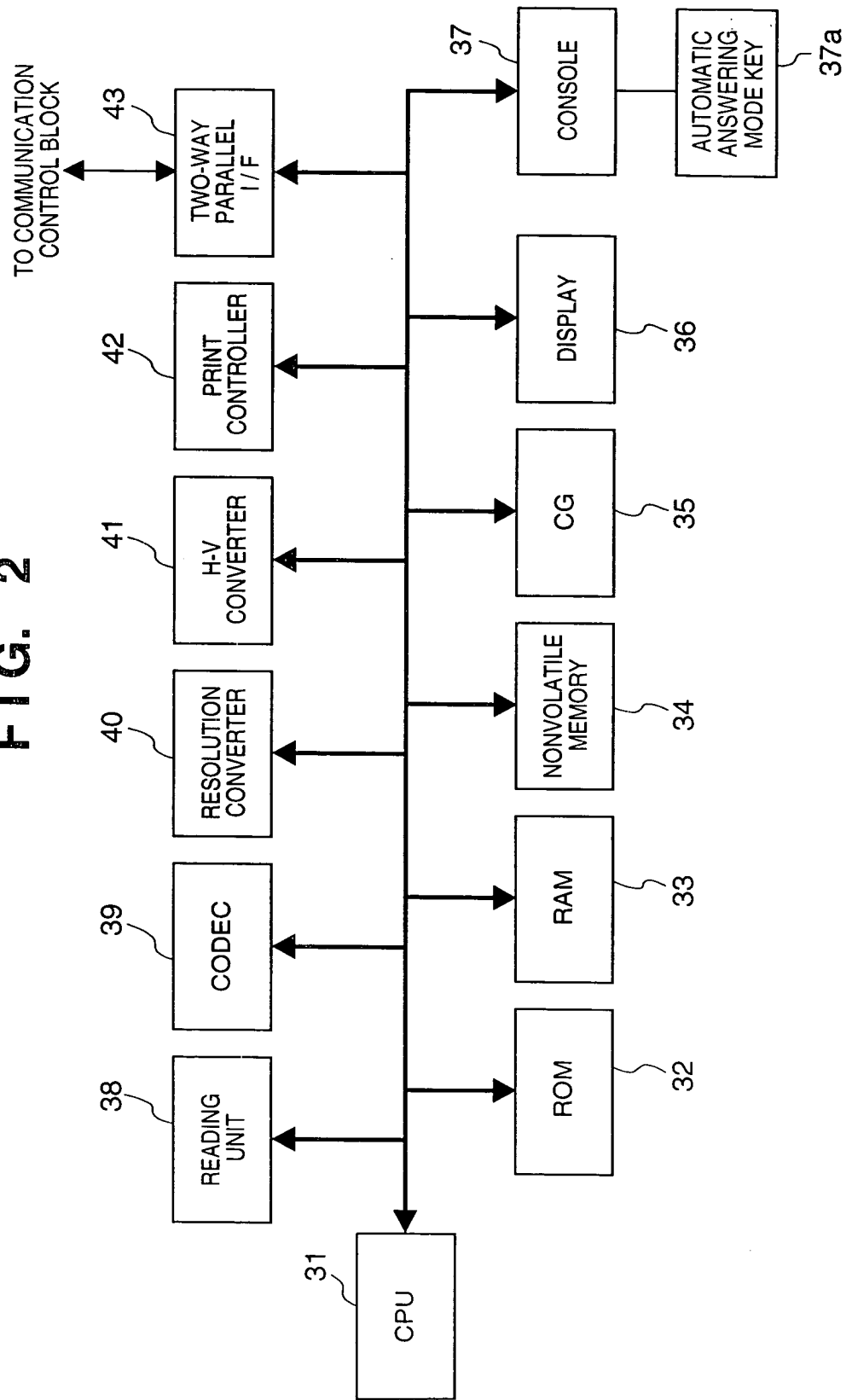


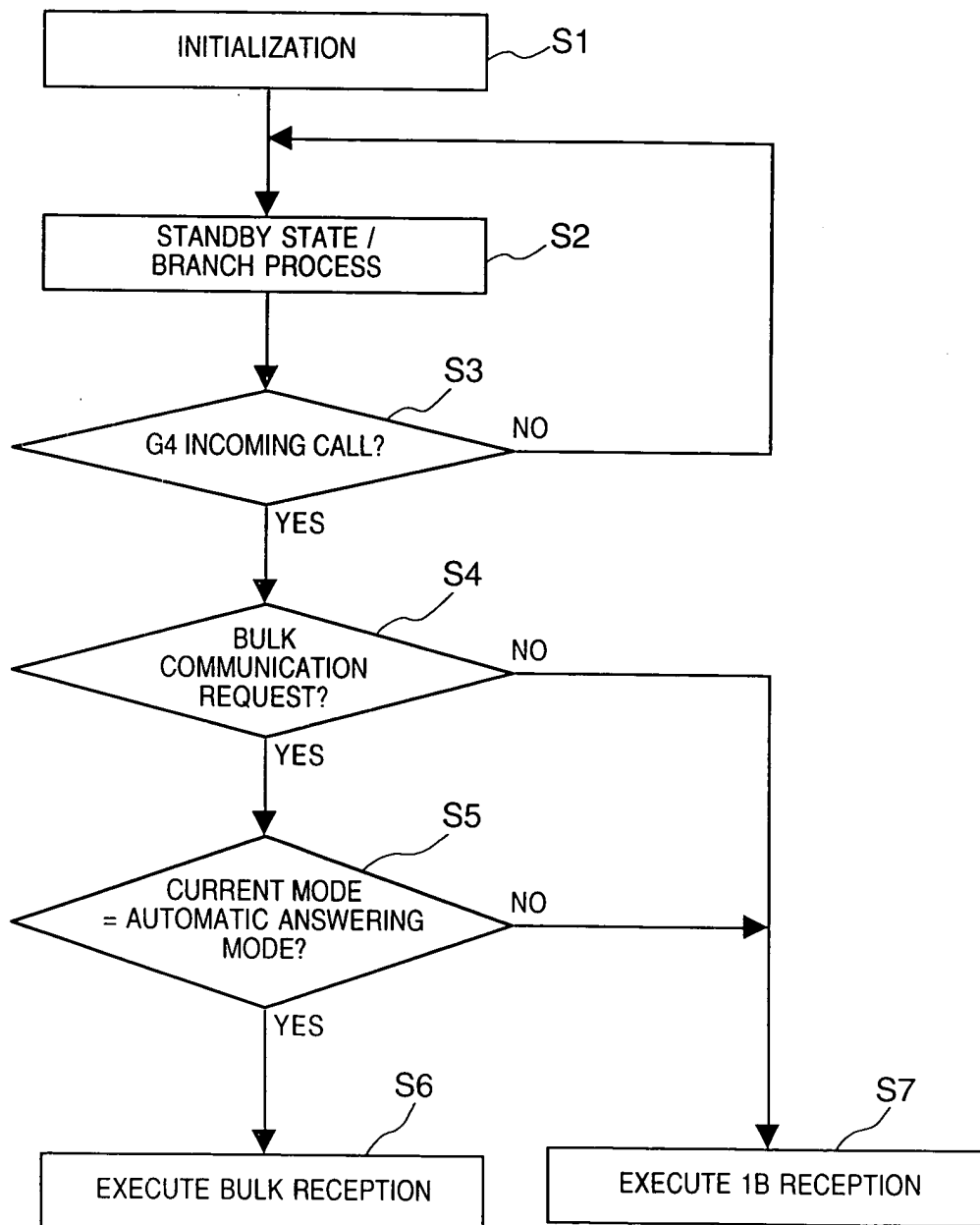
FIG. 3

FIG. 4

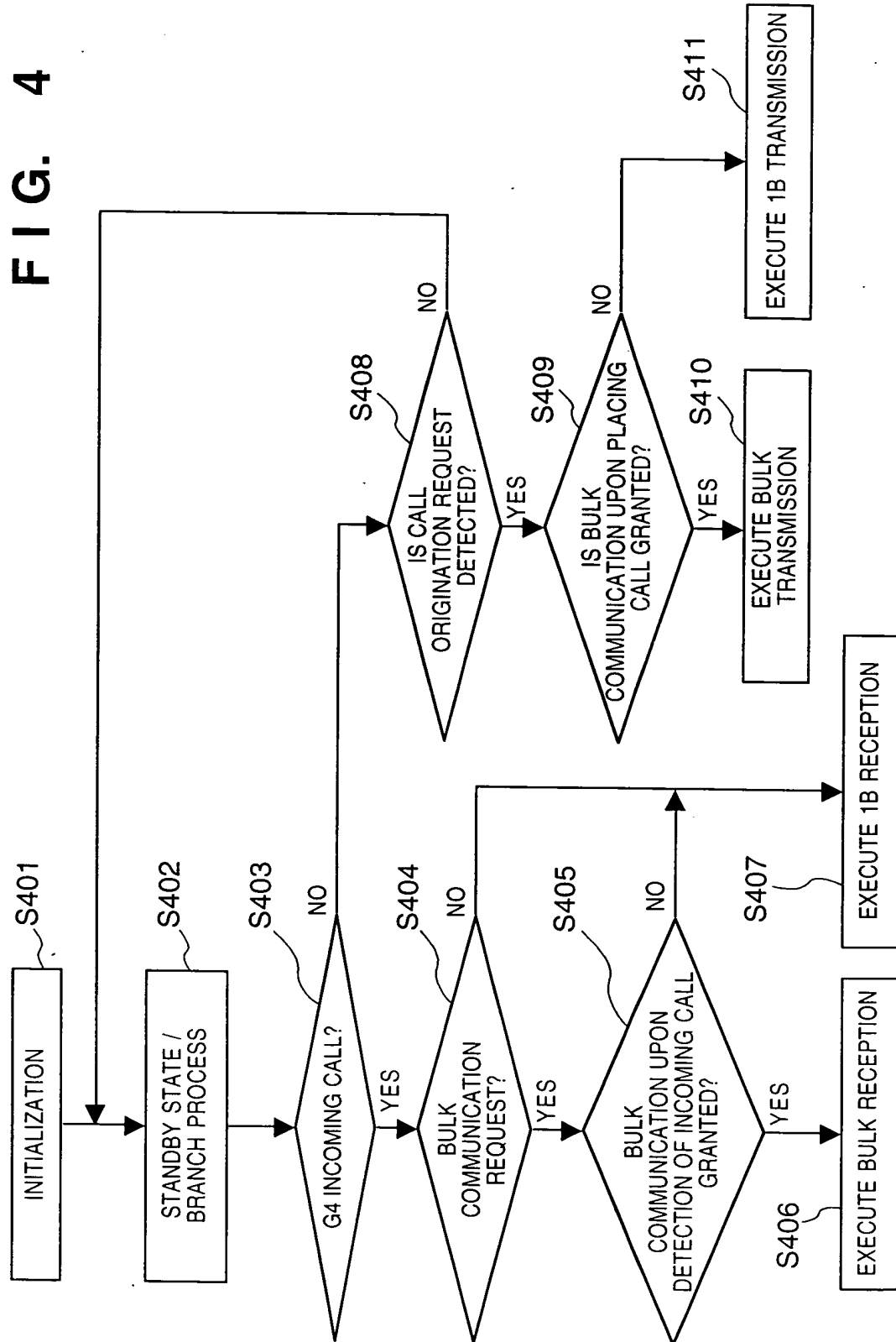


FIG. 5

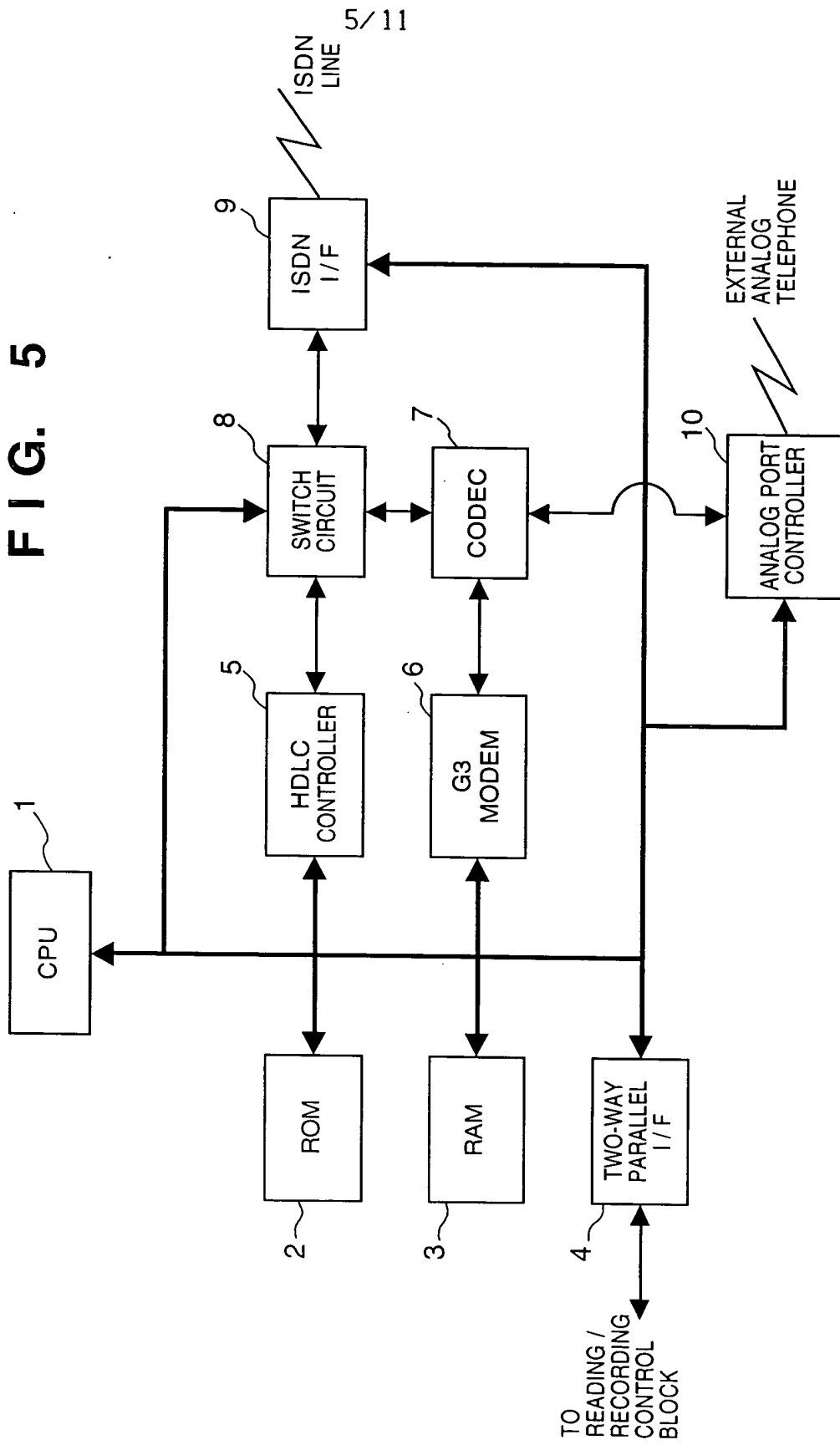


FIG. 6

	TRANSMISSION	RECEPTION
G4 / G3	G4 : GRANT G3 : DENIAL	G4 : GRANT G3 : GRANT
ANALOG PORT 1	DENIAL	GRANT
ANALOG PORT 2	DENIAL	DENIAL
ANALOG PORT 3	GRANT	DENIAL
⋮	⋮	⋮
NO DESIGNATION		DENIAL

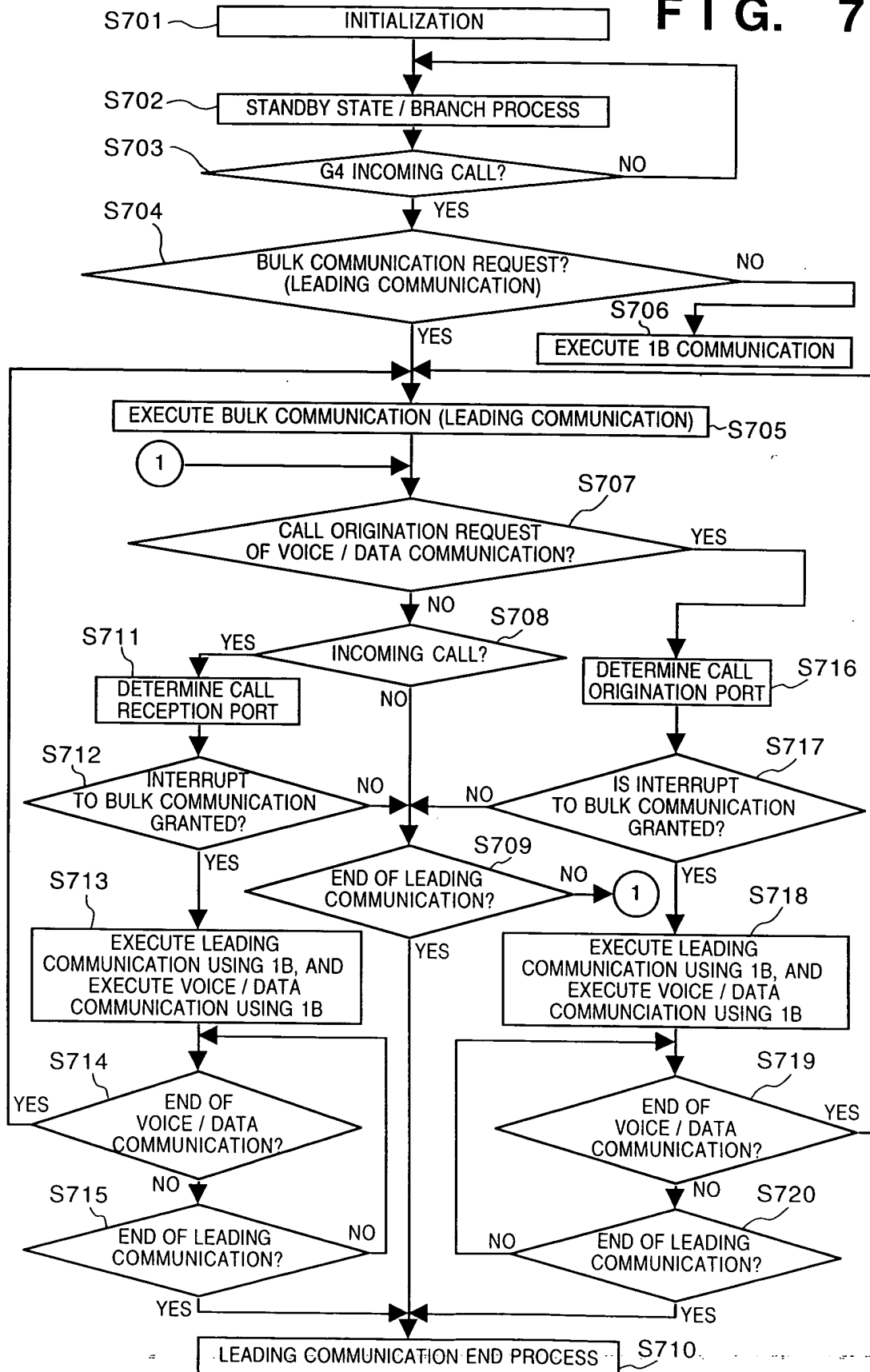


FIG. 8

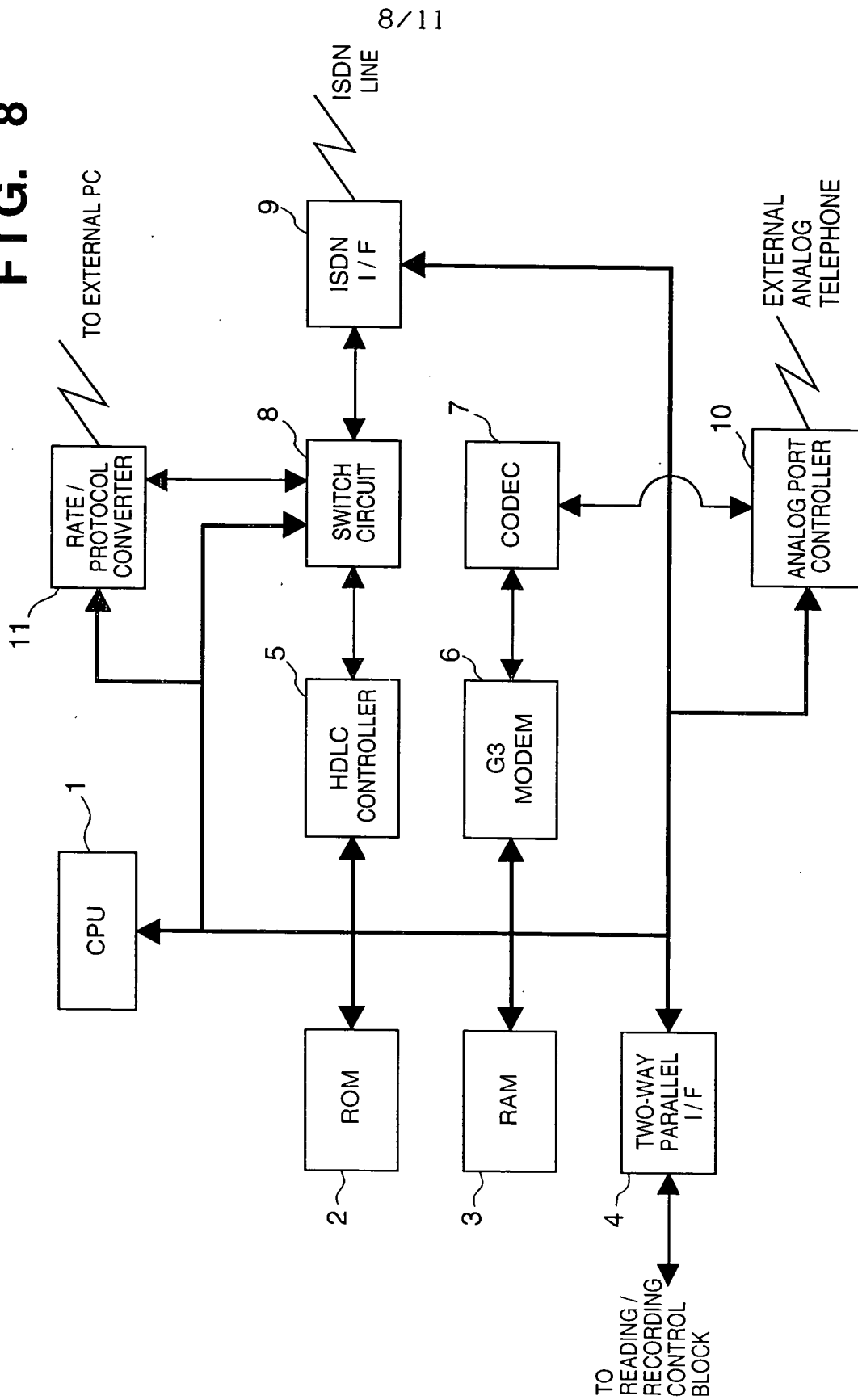


FIG. 9

	TRANSMISSION	RECEPTION
G4 / G3	G4 : GRANT G3 : DENIAL	G4 : GRANT G3 : GRANT
ANALOG PORT 1	DENIAL	GRANT
ANALOG PORT 2	DENIAL	DENIAL
ANALOG PORT 3	GRANT	DENIAL
⋮	⋮	⋮
EXTERNAL PC	DENIAL	GRANT
NO DESIGNATION		DENIAL

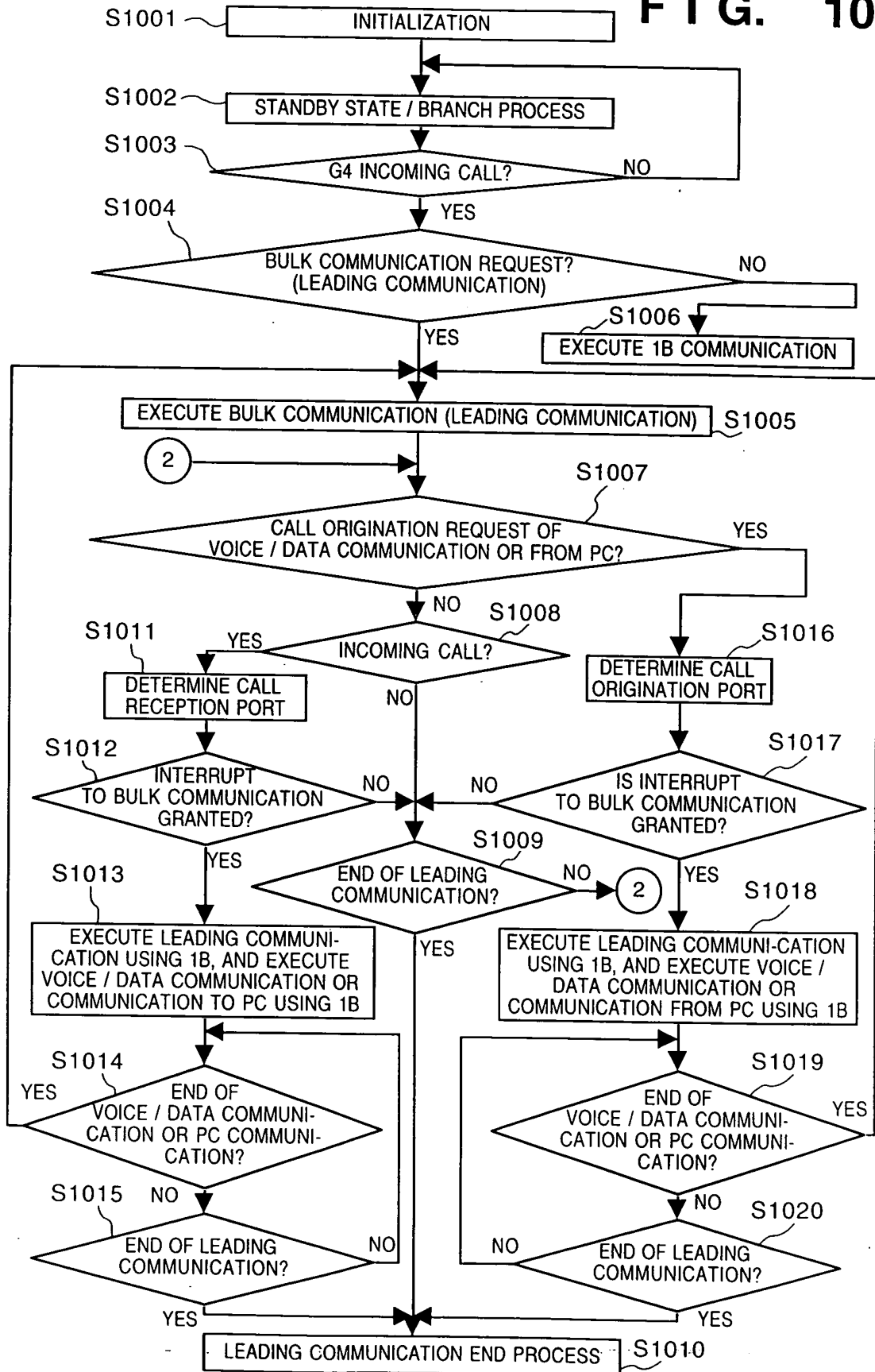


FIG. 11

		TRANSMISSION	RECEPTION
BULK COMMUNICATION UPON RECEPTION	G4 / G3	G4 : GRANT G3 : DENIAL	G4 : GRANT G3 : GRANT
	ANALOG PORT 1	DENIAL	GRANT
	ANALOG PORT 2	:	:
	:	:	:
	EXTERNAL PC	:	:
	NO DESIGNATION		
BULK COMMUNICATION UPON TRANSMISSION	G4 / G3	G4 : DENIAL G3 : DENIAL	G4 : GRANT G3 : GRANT
	ANALOG PORT 1	DENIAL	GRANT
	ANALOG PORT 2	:	:
	:	:	:
	EXTERNAL PC	:	:
	NO DESIGNATION		: